



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hidekazu KAWASHIMA, et al.

Serial No.: 10/755,387

Group Art Unit: 2822

Filed: January 13, 2004

Examiner: Guerrero, Maria F.

For: METHOD AND DECIDE FOR PRODUCING LAYOUT PATTERNS OF A SEMICONDUCTOR DEVICE HAVING AN EVEN WAFER

Honorable Commissioner of Patents
Alexandria, Virginia 22313-1450

**RESPONSE TO FIRST OFFICE ACTION AND
RESTRICTION REQUIREMENT**

Sir:

In response to the Office Action dated September 28, 2005, please consider the following:

INTRODUCTORY COMMENTS

Remarks begin on page 2 of this paper.